

started by interrupts received by the priority-interrupt hardware.

Linked to the interrupt system are the hardware components which protect the computer system from user-issued "privileged" in-

TABLE I. Interrupt priorities.

Priority (Octal)	Source of Interrupt	Report Section Describing Routine
0	unused (<i>highest priority</i>)	
1	high-speed data channel	I-O Processor
2	paper-tape reader	Teletype, etc.
3	line printer	Teletype, etc.
4	I-O controller commands	I-O Processor
5	program-swapping drum	Swapper
6	terminal scanner	Teletype, etc.
7	one-second clock	Dispatcher
10	one-minute clock	Dispatcher
11	unused	
12	paper-tape punch	Teletype, etc.
13	unused	
14	console typewriter	Teletype, etc.
15	I-O processor program	I-O Processor
16	privileged-instruction trap	Dispatcher
17	32-millisecond clock	Swapper